Call for expression of interest for two (2) positions of Hardware Engineers, in the Institute of Computer Science (ICS) Foundation for Research and Technology – Hellas (FORTH)

Position(s): Two (2) positions for the EPI-SGA2 project


Duration: 01/04/2023, 6 months with possibility of extension

Location: Heraklion, Crete, Greece

Opening date: 20/02/2023

Closing date: 02/03/2023

Reference: “Project_EPI_SGA2_2_HW_Engineers_Feb2023”

Description
We seek two (2) Hardware Engineers for our team. The candidates should have proven experience on hardware RTL design and validation with a background in European Research Projects. The candidates will participate in the R&D activities of FORTH in the context of the EuroHPC JU project EPI-SGA2. The European Processor Initiative develops a roadmap for future European high-performance low power processors aligned with the EuroHPC roadmap. EPI focuses on processor technologies based on ARM ISA and architecture for covering the short-term needs and HPC accelerator technologies based on RISC-V, the open-source ISA. RISC-V is also candidate to become the Europa’s own technologies for covering long term objectives. SGA2 also seeks to expand the scope of the project into adjacent European and global vertical markets that can leverage HPC to enable new and improved systems and solutions. The EPI SGA2 proposal, built on top of EPI SGA1 represents the second step toward the implementation of this roadmap. This second step will have a three-year duration with the following objectives: a) Finalize the development and the bring-up of the first generation of low-power processor units developed in SGA1, b) Develop the second generation of the General Purpose Processor (GPP) applying technological enhancements targeting the European Exascale machines with respect to the GPP (Rhea) of SGA1, c) Develop the second generation of low power accelerator test chips, usable by the HPC community for tests and finally d) Develop sound and realistic industrialisation & commercialisation paths and Enable the long-term economical sustainability with an industrialization path in the edge computing area, demonstrated in a few well-chosen proof of concepts like autonomous shuttles or video surveillance.
Requirements:
- Diploma in Computer Science or Computer Engineering.
- Documented professional experience in related roles.
- Experience in Verilog or SystemVerilog RTL design and system development projects.
- Experience in FPGA systems development and Xilinx Vivado Design Suite.
- Experience in European Research Projects.
- Physical presence at FORTH, Heraklion, Crete for the duration of the position.
- Fluent knowledge of English.
- Names of at least three professional references.

Desired qualifications:
- Experience with Network Interfaces, RDMA and protocols such as: AXI or AMBA5 CHI.
- Experience with RISC-V or ARM architectures.
- Experience with test-driven development and validation.

Application Submission
Interested candidates can submit their applications via http://www.ics.forth.gr/jobs/en/ using the link “Apply for the position” under the announcement. Applications must include:
- Detailed CV, including qualifications and interests in the above areas, and proof thereof
- Scanned copies of academic and other titles
Promising candidates may be invited for an interview before a decision is made.

Contact Information
- For information and questions regarding the application and selection procedure, please contact: webmaster@ics.forth.gr
- For information and questions about the advertised positions, the activities of the group, or the Institute, please contact Stelios Louloudakis (slouloudak@ics.forth.gr).

Selection Announcement
The result of the selection will be announced on the website of ICS-FORTH. Candidates have the right to appeal the selection decision, by addressing their written objection to the ICS secretariat within five (5) days since the results announcement on the web. They also have the right to access (a) the files of the candidates as well as (b) the table of candidates’ scores (ranking of candidates results). All the above information related to the selection procedure will be available at the secretariat of ICS-FORTH in line with the Hellenic Data Protection Authority. Access to personal data of co-candidates shall be limited to personal data (and relevant data) and supporting documents which have been the basis of the evaluation of the candidates for the specific post(s). Prior to the announcement of the personal data and/or documents of the co-candidates to the applicant, FORTH will inform the data subjects in an appropriate way.

FORTH is compliant with all legal procedures for the processing of personal data as defined by the Regulation EU/2016/679 on the protection of natural persons with regard to the processing of personal data.
FORTH processes the personal data and relevant supporting documents that you have submitted to us. Processing of that data is carried out exclusively for the needs and purposes of this specific call. Such data shall not be transmitted to or communicated to any third party unless required by law. FORTH retains the above data up to the announcement of the final results of the call, unless further process and reservation is required by law or for purposes of exercise, enforcement, prosecution of certain one’s legitimate legal rights’ as defined in the Regulation EU/2016/679 and/or in national law.
We inform you that under the Regulation EU/2016/679 you have the rights to be informed about your personal data, access to, rectification and erasure, restrictions of process and objection to as provided by applicable regulation and national laws.
We acknowledge also to you, that you have the right to file a complaint to the national Data Protection Authority. For any further information regarding exercise of your personal data protection rights, you may contact the Data Protection Officer at FORTH at dpo@admin.forth.gr.

You have the right to withdraw your application and consent for the processing of your personal data at any time. We inform you that, in this case, FORTH shall destroy such documents and/or supporting documents submitted and shall delete the related personal data.