

ΑΝΑΡΤΗΤΕΑ ΣΤΟ ΔΙΑΔΙΚΤΥΟ



ΕΛΛΗΝΙΚΗ ΔΗΜΟΚΡΑΤΙΑ  
ΥΠΟΥΡΓΕΙΟ ΑΝΑΠΤΥΞΗΣ ΚΑΙ ΕΠΕΝΔΥΣΕΩΝ  
ΓΕΝΙΚΗ ΓΡΑΜΜΑΤΕΙΑ ΕΡΕΥΝΑΣ ΚΑΙ ΤΕΧΝΟΛΟΓΙΑΣ

**ΙΔΡΥΜΑ ΤΕΧΝΟΛΟΓΙΑΣ ΚΑΙ ΕΡΕΥΝΑΣ  
ΙΝΣΤΙΤΟΥΤΟ ΠΛΗΡΟΦΟΡΙΚΗΣ**

Ταχ. Διεύθυνση: Ν. Πλαστήρα 100  
70013 Ηράκλειο Κρήτης

Αρ.Πρωτ. 38913  
Ηράκλειο 13-10-2020

**Call for expression of interest for a position of a Research Associate  
in the Institute of Computer Science (ICS)  
Foundation for Research and Technology – Hellas (FORTH)**



**Position(s):** One (1) position of a Research Associate

**Project:** “THREAT-ARREST: Cyber Security Threats and Threat Actors Training - Assurance Driven Multi-Layer, end-to-end Simulation and Training”, funded by The European Commission (Horizon 2020 - DS-07-2017)

**Desired starting date:** January 1<sup>st</sup>, 2021

**Duration:** 6 months with possibility of extension

**Location:** Heraklion, Crete, Greece

**Opening date:** 13/10/2020

**Closing date:** 29/10/2020

**Ref.:** “THREAT-ARREST-04-Oct-2020”

**Description**

We seek for an experienced member for our team with a strong research background in Computer Science and/or Electrical Engineering. The candidate will participate in the R&D and Management activities of FORTH in the context of the project « THREAT-ARREST: Cyber Security Threats and Threat

Actors Training - Assurance Driven Multi- Layer, end-to-end Simulation and Training.

The candidate will participate in the design and implementation of a cyber-ranges platform, optimizing the parallel computing units for the support of multiple trainee sessions in terms of performance and energy efficacy.

• **Required qualifications:**

- PhD degree in Electronic Engineering or a related field
- More than 5 years of working experience as an R/D engineer/programmer;
- More than 3 years of experience with European research projects both in the technical and management domain;
- Strong background in electronic circuits and digital design
- Willingness and ability to work cooperatively within a team, to learn and to adapt to the projects;
- Fluency in speaking and writing English;

• **Desired qualifications:**

- Experience with European research projects in the ICT domain;
- Research experience - Publications in the field of systems' design and testing;
- Knowledge of Computer Aided Design tools;
- Knowledge on hardware programming and FPGA;
- Knowledge on embedded system design (Software and Hardware);
- Knowledge on highly parallel computing units and energy efficacy.

**Application Submission**

Interested candidates can submit their applications via <http://www.ics.forth.gr/jobs/en/> using the link "[Apply for the position](#)" under the announcement. Applications must include:

- Detailed CV, including qualifications and interests in the above areas, and proof thereof;
- Scanned copies of academic titles;
- Detailed presentation of prior work, studies and/or publications, demonstrating knowledge of desired skills (e.g. experience on specific programming languages and hardware platforms)
- Contact information for 2 or 3 references;

**Contact Information:**

- For information and questions about the advertised position the activity of the group or the Institute, please contact Prof. Sotiris Ioannidis ([sotiris@ics.forth.gr](mailto:sotiris@ics.forth.gr)).

**Selection Announcement**

The result of the selection will be announced on the website of ICS-FORTH. Candidates have the right to appeal the selection decision, by addressing their written objection to the ICS secretariat within five (5) days since the results announcement on the web. They also have the right to access (a) the files of the candidates as well as (b) the table of candidates' scores (ranking of candidates results). All the above information related to the selection procedure will be available at the secretariat of ICS-FORTH in line with the Hellenic Data Protection Authority. Access to personal data of co-candidates shall be limited to personal data (and relevant data) and supporting documents which have been the basis of the evaluation of the candidates for the specific post(s). Prior to the announcement of the personal data and/or documents of the co-candidates to the applicant, FORTH will inform the data subjects in an appropriate way.

FORTH is compliant with all legal procedures for the processing of personal data as defined by the Regulation EU/2016/679 on the protection of natural persons with regard to the processing of personal data.

FORTH processes the personal data and relevant supporting documents that you have submitted to us. Processing of that data is carried out exclusively for the needs and purposes of this specific call. Such data shall not be transmitted to or communicated to any third party unless required by law.

FORTH retains the above data up to the announcement of the final results of the call, unless further process and reservation is required by law or for purposes of exercise, enforcement, prosecution of certain one's legitimate legal rights' as defined in the Regulation EU/2016/679 and/or in national law.

We inform you that under the Regulation EU/2016/679 you have the rights to be informed about your personal data, access to, rectification and erasure, restrictions of process and objection to as provided by applicable regulation and national laws.

We acknowledge also to you, that you have the right to file a complaint to the national Data Protection Authority. For any further information regarding exercise of your personal data protection rights, you may contact the Data Protection Officer at FORTH at [dpo@admin.forth.gr](mailto:dpo@admin.forth.gr).

You have the right to withdraw your application and consent for the processing of your personal data at any time. We inform you that, in this case, FORTH shall destroy such documents and/or supporting documents submitted and shall delete the related personal data.