

## ΑΝΑΡΤΗΤΕΑ ΣΤΟ ΔΙΑΔΙΚΤΥΟ



ΕΛΛΗΝΙΚΗ ΔΗΜΟΚΡΑΤΙΑ  
ΥΠΟΥΡΓΕΙΟ ΠΑΙΔΕΙΑΣ, ΕΡΕΥΝΑΣ ΚΑΙ ΘΡΗΣΚΕΥΜΑΤΩΝ  
ΓΕΝΙΚΗ ΓΡΑΜΜΑΤΕΙΑ ΕΡΕΥΝΑΣ ΚΑΙ ΤΕΧΝΟΛΟΓΙΑΣ

**ΙΔΡΥΜΑ ΤΕΧΝΟΛΟΓΙΑΣ ΚΑΙ ΕΡΕΥΝΑΣ  
ΙΝΣΤΙΤΟΥΤΟ ΠΛΗΡΟΦΟΡΙΚΗΣ**

Ταχ. Διεύθυνση: Ν. Πλαστήρα 100  
70013 Ηράκλειο Κρήτης

Αρ.Πρωτ. 5170  
Ηράκλειο 13-2-2019

**Call for expression of interest for one (1) position Hardware Digital Design and Test Engineer (part-time) with University BSc and MSc in Computer Science or Computer Engineering, in the Institute of Computer Science (ICS) Foundation for Research and Technology – Hellas (FORTH)**



**Position(s):** one (1) position for Hardware Digital Design and Test Engineer (part-time) in the EPI project

**Project:** EPI-SGA1 (GA 826647), funded under H2020 FET-HPC.

**Duration:** 1/4/2019-31/3/2020, 12 months with possibility of extension

**Location:** Heraklion, Crete, Greece

**Opening date:** 13/2/2019

**Closing date:** 28/2/2019

**Reference:** "Projects-EPI\_fpga\_desig\_eng\_Feb2019"

**Description**

We seek one part-time Reconfigurable Hardware Digital Design and Test Engineer for our team, with background in European Research Projects. The candidate will participate in the R&D activities of FORTH in the context of the project EPI: "European Processor Initiative". The EPI SGA1 project will be the first phase of the European Processor Initiative Framework Partnership Agreement, whose aim is to design and implement a roadmap for a new family of low-power European processors for extreme scale computing, high-performance Big-Data and a range of emerging applications.

**Requirements:**

- University BSc and MSc degrees in Computer Science or Computer Engineering.
- Physical presence at FORTH, Heraklion, Crete for the duration of the position.
- Fluent knowledge of English.
- At least 2 years of documented professional experience in related roles.
- Names of at least two professional references

- Experience in high speed serial transceivers and interconnects, e.g. via FPGA's.
- Experience in Accelerators for HPC.
- Experience in system development projects based on Xilinx FPGA's.

**Desired qualifications:**

- Experience with test-driven development and validation.
- Experience in European Research Projects.

**Application Submission**

Interested candidates can submit their applications via <http://www.ics.forth.gr/jobs/en/> using the link "Apply for the position" under the announcement. Applications must include:

- Detailed CV, including qualifications and interests in the above areas, and proof thereof
- Scanned copies of academic titles
- Names of at least two professional references

Promising candidates may be invited for an interview before a decision is made.

**Contact Information**

- For information and questions regarding the application and selection procedure, please contact: [webmaster@ics.forth.gr](mailto:webmaster@ics.forth.gr)
- For information and questions about the advertised positions, the activities of the group, or the Institute, please contact Nikolaos Papadopoulos ([nickpap@ics.forth.gr](mailto:nickpap@ics.forth.gr))

**Selection Announcement**

The result of the selection will be announced on the website of ICS-FORTH. Candidates have the right to appeal the selection decision, by addressing their written objection to the ICS secretariat within five (5) days since the results announcement on the web. They also have the right to access (a) the files of the candidates as well as (b) the table of candidates' scores (ranking of candidates results). All the above information related to the selection procedure will be available at the secretariat of ICS-FORTH in line with the Hellenic Data Protection Authority. Access to personal data of co-candidates shall be limited to personal data (and relevant data) and supporting documents which have been the basis of the evaluation of the candidates for the specific post(s). Prior to the announcement of the personal data and/or documents of the co-candidates to the applicant, FORTH will inform the data subjects in an appropriate way.

FORTH is compliant with all legal procedures for the processing of personal data as defined by the **Regulation EU/2016/679 on the protection of natural persons with regard to the processing of personal data**.

FORTH processes the personal data and relevant supporting documents that you have submitted to us. Processing of that data is carried out exclusively for the needs and purposes of this specific call. Such data shall not be transmitted to or communicated to any third party unless required by law.

FORTH retains the above data up to the announcement of the final results of the call, unless further process and reservation is required by law or for purposes of exercise, enforcement, prosecution of certain one's legitimate legal rights' as defined in the Regulation EU/2016/679 and/or in national law. We inform you that under the **Regulation EU/2016/679** you have the rights to be informed about your personal data, access to, rectification and erasure, restrictions of process and objection to as provided by applicable regulation and national laws.

We acknowledge also to you, that you have the right to file a complaint to the national Data Protection Authority. For any further information regarding exercise of your personal data protection rights, you may contact the Data Protection Officer at FORTH at [dpo@admin.forth.gr](mailto:dpo@admin.forth.gr).

You have the right to withdraw your application and consent for the processing of your personal data at any time. We inform you that, in this case, FORTH shall destroy such documents and/or supporting documents submitted and shall delete the related personal data.